

2007-2008

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY,
HYDERABAD

B.TECH. ELECTRONICS AND COMMUNICATION ENGINEERING

III YEAR

II Semester

Code	Subject	T	P	C
	Management Science	4+1*	-	4
	Telecommunication Switching Systems	4+1*	-	4
	Digital Signal Processing	4+1*	-	4
	VLSI Design	4+1*	-	4
	Microwave Engineering	4+1*	-	4
	Microprocessors and Interfacing	4+1*	-	4
	Electronic Computer Aided Design Lab.	-	3	2
	Advanced English Communication Skills Lab	-	3	2
		30	6	28

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MANAGEMENT SCIENCE

UNIT I

Introduction to Management : Concepts of Management and organization- nature, importance and Functions of Management, Taylor's Scientific Management Theory, Fayol's Principles of Management, Mayo's Hawthorne Experiments, Maslow's Theory of Human Needs, Douglas McGregor's Theory X and Theory Y, Herzberg's Two-Factor Theory of Motivation, Systems Approach to Management, Leadership Styles, Social responsibilities of Management.

UNIT II

Designing Organisational Structures : Basic concepts related to Organisation - Departmentation and Decentralisation, Types of mechanistic and organic structures of organisation (Line organization, Line and staff organization, functional organization, Committee organization, matrix organization, Virtual Organisation, Cellular Organisation, team structure, boundaryless organization, inverted pyramid structure, lean and flat organization structure) and their merits, demerits and suitability.

UNIT III

Operations Management : Principles and Types of Plant Layout-Methods of production (Job, batch and Mass Production), Work Study -Basic procedure involved in Method Study and Work Measurement-Statistical Quality Control: chart, R chart, c chart, p chart, (simple Problems), Acceptance Sampling, Deming's contribution to quality.

UNIT IV

Materials Management : Objectives, Need for Inventory control, EOQ, ABC Analysis, Purchase Procedure, Stores Management and Stores Records.

Marketing : Functions of Marketing, Marketing Mix, Marketing Strategies based on Product Life Cycle, Channels of distribution

UNIT V

Human Resources Management (HRM) : Concepts of HRM, HRD and Personnel Management and Industrial Relations (PMIR), HRM vs.PMIR, Basic functions of HR Manager: Manpower planning, Recruitment, Selection, Training and Development, Placement, Wage and Salary Administration, Promotion, Transfer, Separation, Performance Appraisal, Grievance Handling and Welfare Administration, Job Evaluation and Merit Rating.

UNIT VI

Project Management (PERT/CPM) : Network Analysis, Programme Evaluation and Review Technique (PERT), Critical Path Method (CPM), Identifying critical path, Probability of Completing the project within given time, Project Cost Analysis, Project Crashing. (simple problems)

UNIT VII

Strategic Management : Mission, Goals, Objectives, Policy, Strategy, Programmes, Elements of Corporate Planning Process, Environmental Scanning, Value Chain Analysis, SWOT Analysis, Steps in Strategy Formulation and Implementation, Generic Strategy alternatives.

UNIT VIII

Contemporary Management Practices : Basic concepts of MIS, End User Computing, Materials Requirement Planning (MRP), Just-In-Time (JIT) System, Total Quality Management (TQM), Six sigma and Capability Maturity Model (CMM) Levels, Supply Chain Management, Enterprise Resource Planning (ERP), Performance Management, Business Process outsourcing (BPO), Business Process Re-engineering and Bench Marking, Balanced Score Card.

TEXT BOOKS :

1. Aryasri: *Management Science*, TMH, 2004.
2. Stoner, Freeman, Gilbert, *Management*, 6th Ed, Pearson Education, New Delhi, 2004.

REFERENCES :

1. Kotler Philip & Keller Kevin Lane: *Marketing Mangement* 12/e, PHI, 2005
2. Koontz & Weihrich: *Essentials of Management*, 6/e, TMH, 2005
3. Thomas N.Duening & John M.Ivancevich *Management—Principles and Guidelines*, Biztantra,2003.
4. Kanishka Bedi, *Production and Operations Management*, Oxford University Press, 2004.
5. Memoria & S.V.Gauker, *Personnel Management*, Himalaya, 25/e, 2005

6. Samuel C. Certo: *Modern Management*, 9/e, PHI, 2005
7. Schermerhorn, Capling, Poole & Wiesner: *Management*, Wiley, 2002.
8. Parnell: *Strategic Management*, Biztantra, 2003.
9. Lawrence R Jauch, R.Gupta & William F. Glueck: *Business Policy and Strategic Management*, Frank Bros. 2005.
10. L.S.Srinath: *PERT/CPM*, Affiliated East-West Press, 2005.

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TELECOMMUNICATION SWITCHING SYSTEMS

UNIT I

TELECOMMUNICATION SWITCHING SYSTEMS : Introduction, Elements of switching systems, switching network configuration, principles of cross bar switching.

UNIT II

Electronic space division switching, Time division switching, Combination switching.

UNIT III

TELEPHONE NETWORKS : Subscriber loop systems, switching hierarchy and routing, transmission plan, numbering plan, charging plans.

UNIT IV

SIGNALING TECHNIQUES : In channel signaling, common channel signaling. Network traffic load and parameters, grade of service and blocking probability.

UNIT V

DATA COMMUNICATION NETWORKS : Introduction, network architecture, layered network architecture, protocols, data communications hardware, data communication circuits.

UNIT VI

Public switched data networks, connection oriented & connection less service, Circuit Switching, packet switching and virtual circuit switching concepts, OSI reference model, LAN, WAN, MAN & Internet. Repeaters, Bridges, Routers and gate ways.

UNIT VII

INTEGRATED SERVICES DIGITAL NETWORK (ISDN) : Introduction, motivation, ISDN architecture, ISDN interfaces, functional grouping, reference points, protocol architecture, signaling, numbering, addressing, BISDN.

UNIT VIII

DSL Technology: ADSL, Cable Modem, Traditional Cable Networks, HFC Networks, Sharing, CM & CMTS and DOCSIS.

SONET: Devices, Frame, Frame Transmission, Synchronous Transport Signals, STS I, Virtual Tributaries and Higher rate of service.

TEXT BOOKS :

1. Tele communication switching system and networks - Thyagarajan Viswanath, PHI, 2000.
2. Advanced electronic communications systems - Wayne Tomasi, PHI, 2004.

REFERENCES :

1. Digital telephony - J. Bellamy, John Wiley, 2nd edition, 2001.
2. Data Communications & Networks - Achyut. S.Godbole, TMH, 2004.
3. Principles of Communication Systems – H. Taub & D. Schilling , TMH, 2nd Edition, 2003.
4. Data Communication & Networking - B.A. Forouzan, TMH, 3rd Edition, 2004.
5. Telecommunication switching, Traffic and Networks - J E Flood, Pearson Education, 2002.

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DIGITAL SIGNAL PROCESSING

UNIT I

INTRODUCTION: Introduction to Digital Signal Processing: Discrete time signals & sequences, linear shift invariant systems, stability, and causality. Linear constant coefficient difference equations. Frequency domain representation of discrete time signals and systems.

UNIT II

DISCRETE FOURIER SERIES: Properties of discrete Fourier series, DFS representation of periodic sequences, Discrete Fourier transforms: Properties of DFT, linear convolution of sequences using DFT, Computation of DFT. Relation between Z-transform and DFS

UNIT III

FAST FOURIER TRANSFORMS: Fast Fourier transforms (FFT) - Radix-2 decimation in time and decimation in frequency FFT Algorithms, Inverse FFT, and FFT for composite N

UNIT IV

REALIZATION OF DIGITAL FILTERS: Review of Z-transforms, Applications of Z – transforms, solution of difference equations of digital filters, Block diagram representation of linear constant-coefficient difference equations, Basic structures of IIR systems, Transposed forms, Basic structures of FIR systems, System function,

UNIT V

IIR DIGITAL FILTERS: Analog filter approximations – Butter worth and Chebyshev, Design of IIR Digital filters from analog filters, Design Examples: Analog-Digital transformations

UNIT VI

FIR DIGITAL FILTERS : Characteristics of FIR Digital Filters, frequency response. Design of FIR Digital Filters using Window Techniques, Frequency Sampling technique, Comparison of IIR & FIR filters.

UNIT VII

MULTIRATE DIGITAL SIGNAL PROCESSING: Decimation, interpolation, sampling rate conversion, Implementation of sampling rate conversion.

UNIT VIII

INTRODUCTION TO DSP PROCESSORS: Introduction to programmable DSPs: Multiplier and Multiplier Accumulator (MAC), Modified Bus Structures and Memory Access schemes in DSPs Multiple access memory, multiport memory, VLSI Architecture, Pipelining, Special addressing modes, On-Chip Peripherals.

Architecture of TMS 320C5X- Introduction, Bus Structure, Central Arithmetic Logic Unit, Auxiliary Registrar, Index Registrar, Auxiliary Register Compare Register, Block Move Address Register, Parallel Logic Unit, Memory mapped registers, program controller, Some flags in the status registers, On- chip registers, On-chip peripherals

TEXT BOOKS:

1. Digital Signal Processing, Principles, Algorithms, and Applications: John G. Proakis, Dimitris G. Manolakis, Pearson Education / PHI, 2007.
2. Discrete Time Signal Processing – A.V.Oppenheim and R.W. Schaffer, PHI
3. Digital Signal Processors – Architecture, Programming and Applications,, B.Venkataramani, M. Bhaskar, TATA McGraw Hill, 2002

Reference Books:

1. Digital Signal Processing: Andreas Antoniou, TATA McGraw Hill , 2006
2. Digital Signal Processing: MH Hayes, Schaum's Outlines, TATA Mc-Graw Hill, 2007.
3. DSP Primer - C. Britton Rorabaugh, Tata McGraw Hill, 2005.
4. Fundamentals of Digital Signal Processing using Matlab – Robert J. Schilling, Sandra L. Harris, Thomson, 2007.
5. Digital Signal Processing – Alan V. Oppenheim, Ronald W. Schafer, PHI Ed., 2006

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VLSI DESIGN

UNIT I

INTRODUCTION : Introduction to IC Technology – MOS, PMOS, NMOS, CMOS & BiCMOS technologies- Oxidation, Lithography, Diffusion, Ion implantation, Metallisation, Encapsulation, Probe testing, Integrated Resistors and Capacitors.

UNIT II

BASIC ELECTRICAL PROPERTIES : Basic Electrical Properties of MOS and BiCMOS Circuits: Ids-Vds relationships, MOS transistor threshold Voltage, gm, gds, figure of merit η_0 ; Pass transistor, NMOS Inverter, Various pull ups, CMOS Inverter analysis and design, Bi-CMOS Inverters.

UNIT III

VLSI CIRCUIT DESIGN PROCESSES : VLSI Design Flow, MOS Layers, Stick Diagrams, Design Rules and Layout, 2 μ m CMOS Design rules for wires, Contacts and Transistors Layout Diagrams for NMOS and CMOS Inverters and Gates, Scaling of MOS circuits, Limitations of Scaling.

UNIT IV

GATE LEVEL DESIGN : Logic Gates and Other complex gates, Switch logic, Alternate gate circuits, Basic circuit concepts, Sheet Resistance R_S and its concept to MOS, Area Capacitance Units, Calculations - t_{pd} -Delays, Driving large Capacitive Loads, Wiring Capacitances, Fan-in and fan-out, Choice of layers

UNIT V

SUBSYSTEM DESIGN : Subsystem Design, Shifters, Adders, ALUs, Multipliers, Parity generators, Comparators, Zero/One Detectors, Counters, High Density Memory Elements.

UNIT VI

SEMICONDUCTOR INTEGRATED CIRCUIT DESIGN : PLAs, FPGAs, CPLDs, Standard Cells, Programmable Array Logic, Design Approach.

UNIT VII

VHDL SYNTHESIS : VHDL Synthesis, Circuit Design Flow, Circuit Synthesis, Simulation, Layout, Design capture tools, Design Verification Tools, Test Principles.

UNIT VIII

CMOS TESTING : CMOS Testing, Need for testing, Test Principles, Design Strategies for test, Chiplevel Test Techniques, System-level Test Techniques, Layout Design for improved Testability.

TEXTBOOKS :

1. Essentials of VLSI circuits and systems – Kamran Eshraghian, Eshraghian Douglas and A. Pucknell, PHI, 2005 Edition.
2. Principles of CMOS VLSI Design - Weste and Eshraghian, Pearson Education, 1999.

REFERENCES :

1. Chip Design for Submicron VLSI: CMOS Layout & Simulation, - John P. Uyemura, Thomson Learning.
2. Introduction to VLSI Circuits and Systems - John .P. Uyemura, JohnWiley, 2003.
3. Digital Integrated Circuits - John M. Rabaey, PHI, EEE, 1997.
4. Modern VLSI Design - Wayne Wolf, Pearson Education, 3rd Edition, 1997.
5. VLSI Technology – S.M. SZE, 2nd Edition, TMH, 2003.

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MICROWAVE ENGINEERING

UNIT I

MICROWAVE TRANSMISSION LINES: Introduction, Microwave Spectrum and Bands, Applications of Microwaves. *Rectangular Waveguides – TE/TM mode analysis, Expressions for Fields, Characteristic Equation and Cut-off Frequencies, Filter Characteristics, Dominant and Degenerate Modes, Sketches of TE and TM mode fields in the cross-section, Mode Characteristics – Phase and Group Velocities, Wavelengths and Impedance Relations; Power Transmission and Power Losses in Rectangular Guide. Related Problems.*

UNIT II

CIRCULAR WAVEGUIDES: Introduction, Nature of Fields, Characteristic Equation, Dominant and Degenerate Modes. Impossibility of TEM mode. Microstrip Lines– Introduction, Zo Relations, Effective Dielectric Constant, Losses, Q factor. Cavity Resonators– Introduction, Rectangular and Cylindrical Cavities, Dominant Modes and Resonant Frequencies, Q factor and Coupling Coefficients. Related Problems.

UNIT III

WAVEGUIDE COMPONENTS AND APPLICATIONS - I : Coupling Mechanisms – Probe, Loop, Aperture types. Waveguide Discontinuities – Waveguide irises, Tuning Screws and Posts, Matched Loads. Waveguide Attenuators – Resistive Card, Rotary Vane types; Waveguide Phase Shifters – Dielectric, Rotary Vane types. Waveguide Multiport Junctions – E plane and H plane Tees, Magic Tee, Hybrid Ring; Directional Couplers – 2 Hole, Bethe Hole types.

UNIT IV

WAVEGUIDE COMPONENTS AND APPLICATIONS - II : Ferrites– Composition and Characteristics, Faraday Rotation; Ferrite Components – Gyrator, Isolator, Circulator. Scattering Matrix– Significance, Formulation and Properties. S Matrix Calculations for – 2 port Junction, E plane and H plane Tees, Magic Tee, Directional Coupler, Circulator and Isolator. Related Problems.

UNIT V

MICROWAVE TUBES – I: Limitations and Losses of conventional tubes at microwave frequencies. Microwave tubes – O type and M type classifications. O-type tubes : 2 Cavity Klystrons – Structure, Reentrant Cavities, Velocity Modulation Process and Applegate Diagram, Bunching Process and Small Signal Theory – Expressions for o/p Power and Efficiency. Reflex Klystrons – Structure, Applegate Diagram and Principle of working, Mathematical Theory of Bunching, Power Output, Efficiency, Electronic Admittance; Oscillating Modes and o/p Characteristics, Electronic and Mechanical Tuning. Related Problems.

UNIT VI

HELIX TWTS: Significance, Types and Characteristics of Slow Wave Structures; Structure of TWT and Amplification Process (qualitative treatment), Suppression of Oscillations, Nature of the four Propagation Constants, Gain Considerations.

M-type Tubes

Introduction, Cross-field effects, Magnetrons – Different Types, 8-Cavity Cylindrical Travelling Wave Magnetron – Hull Cut-off and Hartree Conditions, Modes of Resonance and PI-Mode Operation, Separation of PI-Mode, o/p characteristics.

UNIT VII

MICROWAVE SOLID STATE DEVICES: Introduction, Classification, Applications. TEDs – Introduction, Gunn Diode – Principle, RWH Theory, Characteristics, Basic Modes of Operation, Oscillation Modes. Avalanche Transit Time Devices – Introduction, IMPATT and TRAPATT Diodes – Principle of Operation and Characteristics.

UNIT VIII

MICROWAVE MEASUREMENTS: Description of Microwave Bench – Different Blocks and their Features, Precautions; Microwave Power Measurement – Bolometer Method. Measurement of Attenuation, Frequency, VSWR, Cavity Q. Impedance Measurements.

TEXT BOOKS :

1. Microwave Devices and Circuits – Samuel Y. Liao, PHI, 3rd Edition, 1994.
2. Microwave Principles – Herbert J. Reich, J.G. Skalnik, P.F. Ordung and H.L. Krauss, CBS Publishers and Distributors, New Delhi, 2004.

REFERENCES :

1. Foundations for Microwave Engineering – R.E. Collin, IEEE Press, John Wiley, 2nd Edition, 2002.
2. Microwave Circuits and Passive Devices – M.L. Sisodia and G.S. Raghuvanshi, Wiley Eastern Ltd., New Age International Publishers Ltd., 1995.
3. Microwave Engineering Passive Circuits – Peter A. Rizzi, PHI, 1999.
4. Electronic and Radio Engineering – F.E. Terman, McGraw-Hill, 4th ed., 1955.
5. Elements of Microwave Engineering – R. Chatterjee, Affiliated East-West Press Pvt. Ltd., New Delhi, 1988.
6. Micro Wave and Radar Engineering – M. Kulkarni, Umesh Publications, 1998.

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MICROPROCESSORS AND INTERFACING

UNIT-I

An over view of 8085, Architecture of 8086 Microprocessor. Special functions of General purpose registers. 8086 flag register and function of 8086 Flags. Addressing modes of 8086. Instruction set of 8086. Assembler directives, simple programs, procedures, and macros.

UNIT-II

Assembly language programs involving logical, Branch & Call instructions, sorting, evaluation of arithmetic expressions, string manipulation.

UNIT-III

Pin diagram of 8086-Minimum mode and maximum mode of operation. Timing diagram. Memory interfacing to 8086 (Static RAM & EPROM). Need for DMA. DMA data transfer Method. Interfacing with 8237/8257.

UNIT-IV

8255 PPI – various modes of operation and interfacing to 8086. Interfacing Keyboard, Displays, 8279 Stepper Motor and actuators. D/A and A/D converter interfacing.

UNIT-V

Interrupt structure of 8086. Vector interrupt table. Interrupt service routines. Introduction to DOS and BIOS interrupts. 8259 PIC Architecture and interfacing cascading of interrupt controller and its importance.

UNIT-VI

Serial data transfer schemes. Asynchronous and Synchronous data transfer schemes. 8251 USART architecture and interfacing. TTL to RS 232C and RS232C to TTL conversion. Sample program of serial data transfer. Introduction to High-speed serial communications standards, USB.

UNIT-VII

Advanced Micro Processors - Introduction to 80286, Salient Features of 80386, Real and Protected Mode Segmentation & Paging, Salient Features of Pentium, Branch Prediction, Overview of RISC Processors.

UNIT-VIII

8051 Microcontroller Architecture, Register set of 8051, Modes of timer operation, Serial port operation, Interrupt structure of 8051, Memory and I/O interfacing of 8051.

TEXT BOOKS :

1. Advanced microprocessor and Peripherals - A.K.Ray and K.M.Bhurchandi, TMH, 2000.
2. Micro Controllers – Deshmukh, Tata McGraw Hill Edition.

REFERENCES :

1. Micro Processors & Interfacing – Douglas U. Hall, 2007.
2. The 8088 and 8086 Micro Processors – PHI, 4th Edition, 2003.
3. Micro Computer System 8086/8088 Family Architecture, Programming and Design - By Liu and GA Gibson, PHI, 2nd Ed.,

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MICROPROCESSORS AND INTERFACING LAB

I. Microprocessor 8086 :

1. Introduction to MASM/TASM.
2. Arithmetic operation – Multi byte Addition and Subtraction, Multiplication and Division – Signed and unsigned Arithmetic operation, ASCII – arithmetic operation.
3. Logic operations – Shift and rotate – Converting packed BCD to unpacked BCD, BCD to ASCII conversion.
4. By using string operation and Instruction prefix: Move Block, Reverse string, Sorting, Inserting, Deleting, Length of the string, String comparison.
5. DOS/BIOS programming: Reading keyboard (Buffered with and without echo) – Display characters, Strings.

II. Interfacing :

1. 8259 – Interrupt Controller : Generate an interrupt using 8259 timer.
2. 8279 – Keyboard Display : Write a small program to display a string of characters.
3. 8255 – PPI : Write ALP to generate sinusoidal wave using PPI.
4. 8251 – USART : Write a program in ALP to establish Communication between two processors.

III. Microcontroller 8051

1. Reading and Writing on a parallel port.
2. Timer in different modes.
3. Serial communication implementation.

Equipment required for Laboratories:

1. 8086 μ P Kits
2. 8051 Micro Controller kits
3. Interfaces/peripheral subsystems
 - i) 8259 PIC
 - ii) 8279-KB/Display
 - iii) 8255 PPI
 - iv) 8251 USART
4. ADC Interface
5. DAC Interface
6. Traffic Controller Interface
7. Elevator Interface

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ADVANCED ENGLISH COMMUNICATION SKILLS LAB

1. Introduction

The introduction of the English Language Lab is considered essential at 3rd year level. At this stage the students need to prepare themselves for their careers which may require them to listen to, read, speak and write in English both for their professional and interpersonal communication in the globalised context.

The proposed course should be an integrated theory and lab course to enable students to use 'good' English and perform the following:

- Gather ideas and information, to organise ideas relevantly and coherently.
- Engage in debates.
- Participate in group discussions.
- Face interviews.
- Write project/research reports/technical reports.
- Make oral presentations.
- Write formal letters.
- Transfer information from non-verbal to verbal texts and vice versa.
- To take part in social and professional communication.

2. Objectives:

This Lab focuses on using computer-aided multimedia instruction for language development to meet the following targets:

- To improve the students' fluency in English, through a well-developed vocabulary and enable them to listen to English spoken at normal conversational speed by educated English speakers and respond appropriately in different socio-cultural and professional contexts.
- Further, they would be required to communicate their ideas relevantly and coherently in writing.

3. Syllabus:

The following course content is prescribed for the Advanced Communication Skills Lab:

- Functional English - starting a conversation – responding appropriately and relevantly – using the right body language – role play in different situations.
- Vocabulary building – synonyms and antonyms, word roots, one-word substitutes, prefixes and suffixes, study of word origin, analogy, idioms and phrases.
- Group Discussion – dynamics of group discussion, intervention, summarizing, modulation of voice, body language, relevance, fluency and coherence.
- Interview Skills – concept and process, pre-interview planning, opening strategies, answering strategies, interview through tele and video-conferencing.
- Resume' writing – structure and presentation, planning, defining the career objective, projecting ones strengths and skill-sets, summary, formats and styles, letter-writing.
- Reading comprehension – reading for facts, guessing meanings from context, scanning, skimming, inferring meaning, critical reading.
- Technical Report writing – Types of formats and styles, subject matter – organization, clarity, coherence and style, planning, data-collection, tools, analysis.

4. Minimum Requirement:

The English Language Lab shall have two parts:

- The Computer aided Language Lab** for 60 students with 60 systems, one master console, LAN facility and English language software for self- study by learners.
- The Communication Skills Lab** with movable chairs and audio-visual aids with a P.A System, a T. V., a digital stereo –audio & video system and camcorder etc.

System Requirement (Hardware component):

Computer network with Lan with minimum 60 multimedia systems with the following specifications:

- P – IV Processor
 - Speed – 2.8 GHZ
 - RAM – 512 MB Minimum
 - Hard Disk – 80 GB
- Headphones of High quality

5. Suggested Software:

The software consisting of the prescribed topics elaborated above should be procured and used.

Suggested Software:

- **Clarity Pronunciation Power** – part II
- **Oxford Advanced Learner's Compass**, 7th Edition
- **DELTA's key to the Next Generation TOEFL Test: Advanced Skill Practice.**
- **Lingua TOEFL CBT Insider**, by Dreamtech
- **TOEFL & GRE**(KAPLAN, AARCO & BARRONS, USA, Cracking GRE by CLIFFS)
- **The following software from 'train2success.com'**
 - **Preparing for being Interviewed,**
 - **Positive Thinking,**
 - **Interviewing Skills,**
 - **Telephone Skills,**
 - **Time Management**
 - **Team Building,**
 - **Decision making**

- **English in Mind**, Herbert Puchta and Jeff Stranks with Meredith Levy, Cambridge

6. Books Recommended:

1. **Effective Technical Communication**, M. Ashraf Rizvi, Tata Mc. Graw-Hill Publishing Company Ltd.
2. **A Course in English communication** by Madhavi Apte, Prentice-Hall of India, 2007.
3. **Communication Skills** by Leena Sen, Prentice-Hall of India, 2005.
4. **Academic Writing- A Practical guide for students** by Stephen Bailey, Rontledge Falmer, London & New York, 2004.
5. **English Language Communication : A Reader cum Lab Manual** Dr A Ramakrishna Rao, Dr G Natanam & Prof SA Sankaranarayanan, Anuradha Publications, Chennai
6. **Body Language- Your Success Mantra** by Dr. Shalini Verma, S. Chand, 2006.
7. **DELTA's key to the Next Generation TOEFL Test: Advanced Skill Practice**, New Age International (P) Ltd., Publishers, New Delhi.
8. Books on **TOEFL/GRE/GMAT/CAT** by Barron's/cup
9. **IELTS series with CDs** by Cambridge University Press.
10. **Technical Report Writing Today** by Daniel G. Riordan & Steven E. Pauley, Biztantra Publishers, 2005.
11. **Basic Communication Skills for Technology** by Andra J. Rutherford, 2nd Edition, Pearson Education, 2007.
12. **Communication Skills for Engineers** by Sunita Mishra & C. Muralikrishna, Pearson Education, 2007.
13. **Objective English** by Edgar Thorpe & Showick Thorpe, 2nd edition, Pearson Education, 2007.
14. **Cambridge Preparation for the TOEFL Test** by Jolene Gear & Robert Gear, 4th Edition.
15. **Technical Communication** by Meenakshi Raman & Sangeeta Sharma, Oxford University Press.

DISTRIBUTION AND WEIGHTAGE OF MARKS:

Advanced Communication Skills Lab Practicals:

1. The practical examinations for the English Language Laboratory practice shall be conducted as per the University norms prescribed for the core engineering practical sessions.
2. For the English Language lab sessions, there shall be a continuous evaluation during the year for 25 sessional marks and 50 End Examination marks. Of the 25 marks, 15 marks shall be awarded for day-to-day work and 10 marks to be awarded by conducting Internal Lab Test(s). The End Examination shall be conducted by the teacher concerned with the help of another member of the staff of the same department of the same institution.